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Pat. No. 05983926 - 6  
Issue Date: 07/19/04

Group ID: F Page 1  
User ID: Caleahy KS: 3,248

Warning [Pages Of US References:]

page 2 has no references  
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page 8 has no references  
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Warning [Pages Of Foreign References:]

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page 7 has no references

32/32

CHECK LIST

Rule 47      Continuing Data      PCT      Disclaimer

No              No              No              No

Microfiche Appendix      CPA tag

No              No

Foreign Priority Claimed: No  
Acknowledged: No

State Code:      Country Code: FRX

Text Endorsement: 09882285.061801

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JACKET

<u>SERIAL NUMBER</u>	<u>FILING DATE</u>	<u>CLASS</u>	<u>SUBCLASS</u>	<u>GAU</u>
09/882,285	06/18/01	712	228	2183

FOREIGN PRIORITY  
Country      Document Number      Date

DISCLAIMER

/ /

TITLE

Instruction specified register value saving in allocated caller stack  
or not yet allocated callee stack

MICROFICHE APPENDIX

ASSISTANT EXAMINER:

First:                      Middle:                      Last:

PRIMARY EXAMINER:

First:                      Middle:                      Last:

Kenneth                      S.                      Kim

CLAIMS ALLOWED  
Total      Print

30            1

DRAWINGS  
Sheets      Figures      Print

22            22            Y

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BLUE SLIP INFORMATION

<u>SERIAL NUMBER</u>	<u>CLASS</u>	<u>SUBCLASS</u>	<u>GAU</u>
09/882,285	712	228	2183

<u>INDEP. CLAIMS</u>	<u>TOTAL CLAIMS</u>
1, 12, 14, 17, 20, 23, 26, 30, 6, 9	30

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BLUE SLIP (Page 1)

INTERNATIONAL CLASSIFICATION  
Class      SubClass

G06F      9/40

CROSS-REFERENCES  
Class      SubClass

712      229

718      108

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TERM EXTENSION

548

FIELD OF SEARCH

Class      SubClass

718      108

712 228;229

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OATH

INVENTOR NAME

First: Middle: Last: Signed:

Kevin D. Kissell Yes

City: Le Bar sur Loup

State: ZIP Code: Country: FRX Foreign ZIP:

INVENTOR NAME

First: Middle: Last: Signed:

Hartvig W. J. Ekner Yes

City: Ballerup

State: ZIP Code: Country: DKX Foreign ZIP:

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PCT INFO

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CONTINUING DATA (Page 1)

LINE CODE SERIAL NUMBER FILING DATE STATUS DOCUMENT NO. ISSUE DATE

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REFERENCES (Page 1) SERIAL NUMBER: 09/882,285  
FORM 892

U.S. REFERENCES

<u>U.S. Pat No.</u>	<u>Date</u>	<u>Patentee</u>	<u>Class</u>	<u>SubClass</u>
5,517,664	05/1996	Watanabe et al.	712	41
*6,212,630	04/2001	Takayama et al.	712	242
No issue date available.				
5,241,679	08/1993	Nakagawa et al.	710	260

2002/0138715 09/2002 Minematsu

712 225

FOREIGN REFERENCES

<u>Foreign Doc No.</u>	<u>Date</u>	<u>Country</u>	<u>Class</u>	<u>SubClass</u>
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OTHER REFERENCE CITATIONS (incl. Author, Title, Date, Pertinent Pages, etc.)

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REFERENCES (Page 2) SERIAL NUMBER: 09/882,285  
FORM 1449

U.S. REFERENCES

<u>U.S. Pat No.</u>	<u>Date</u>	<u>Patentee</u>	<u>Class</u>	<u>SubClass</u>
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FOREIGN REFERENCES

<u>Foreign Doc No.</u>	<u>Date</u>	<u>Country</u>	<u>Class</u>	<u>SubClass</u>
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OTHER REFERENCE CITATIONS (incl. Author, Title, Date, Pertinent Pages, etc.)

[i ARM Architecture Reference Manual[1 , Advanced RISC Machines Ltd.,  
Prentice Hall, pp. 3-41, 3-42, 3-43, 3-67, and 3-68 (1996).

=====

REFERENCES (Page 3) SERIAL NUMBER: 09/882,285  
FORM 1449

U.S. REFERENCES

<u>U.S. Pat No.</u>	<u>Date</u>	<u>Patentee</u>	<u>Class</u>	<u>SubClass</u>
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FOREIGN REFERENCES

<u>Foreign Doc No.</u>	<u>Date</u>	<u>Country</u>	<u>Class</u>	<u>SubClass</u>
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OTHER REFERENCE CITATIONS (incl. Author, Title, Date, Pertinent Pages, etc.)

Diefendorff, Keith, et al., [37 Organization of the Motorola 88110  
Superscalar RISC Microprocessor,[38 [0 [i IEEE Micro[1 , 12:2, pp.  
40-63 (Apr. 1992).

-----

Diefendorff, Keith, et al., [37 AltiVec Extension to PowerPC  
Accelerates Media Processing,[38 [0 [i IEEE Micro[1 , 20:2, pp. 85-95  
(Mar.-Apr. 2000).

-----

[i ESA/390 Principles of Operation[1 , IBM Library Server, Table of  
Contents and Paragraphs 7.5.31 and 7.5.70, 23 pages, at  
[=====

REFERENCES \(Page 4\) SERIAL NUMBER: 09/882,285  
FORM 1449](http://publibz.boulder.ibm.com/cgi-bin/bookmgr[hd [13 [1<br/>OS390/BOOK/DZ9AR001/CCONTENTS (1993).</p></div><div data-bbox=)

U.S. REFERENCES

<u>U.S. Pat No.</u>	<u>Date</u>	<u>Patentee</u>	<u>Class</u>	<u>SubClass</u>
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FOREIGN REFERENCES

<u>Foreign Doc No.</u>	<u>Date</u>	<u>Country</u>	<u>Class</u>	<u>SubClass</u>
------------------------	-------------	----------------	--------------	-----------------

OTHER REFERENCE CITATIONS (incl. Author, Title, Date, Pertinent Pages, etc.)

Kane, Gerry, [i PA-RISC 2.0 Architecture[1 , Prentice Hall PTR, Upper  
Saddle River, New Jersey, pp. 7-106 and 7-107(1996).

-----

May, Cathy, et al., eds., [i The PowerPC[198 [0 Architecture: A  
Specification for a New Family of RISC Processors[1 , Second Edition,  
Morgan Kaufmann Publishers, Inc., San Francisco, California, pp. 70-72  
(May 1994).

-----

[i MC68020 32-Bit Microprocessor User[3 s Manual[1 , Third Edition,  
Prentice Hall, Englewood Cliffs, New Jersey, pp. 3-125, 3-126, and  
3-127 (1989).

=====

REFERENCES (Page 5) SERIAL NUMBER: 09/882,285  
FORM 1449

U.S. REFERENCES

<u>U.S. Pat No.</u>	<u>Date</u>	<u>Patentee</u>	<u>Class</u>	<u>SubClass</u>
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FOREIGN REFERENCES

<u>Foreign Doc No.</u>	<u>Date</u>	<u>Country</u>	<u>Class</u>	<u>SubClass</u>
------------------------	-------------	----------------	--------------	-----------------

OTHER REFERENCE CITATIONS (incl. Author, Title, Date, Pertinent Pages, etc.)

[i MC88110 Second Generation RISC Microprocessor User[3 s Manual[1 ,  
Motorola, Inc., pp. 10-66, 10-67, and 10-71 (1991).

-----  
Nemirovsky, Mario, et al., U.S. application No. 09/629,805; entitled  
[37 Method and Apparatus for Improved Computer Load and Store  
Operations,[38 [0 filed Jul. 31, 2000, 41 pages.

=====  
REFERENCES (Page 6) SERIAL NUMBER: 09/882,285  
FORM 1449

U.S. REFERENCES

<u>U.S. Pat No.</u>	<u>Date</u>	<u>Patentee</u>	<u>Class</u>	<u>SubClass</u>
4,338,663	07/1982	Strecker et al.	712	228
4,833,640	05/1989	Baba	718	108
5,031,096	07/1991	Jen et al.	711	169
5,109,495	04/1992	Fite et al.	712	207
<1,3>5,241,679	08/1993	Nakagawa et al.		
5,327,566	07/1994	Forsyth	710	260
5,442,761	08/1995	Toda et al.	712	205
5,493,687	02/1996	Garg et al.	712	23
<1,1>5,517,664	05/1996	Watanabe et al.		
5,524,211	06/1996	Woods et al.	709	220
5,542,060	07/1996	Yoshida	712	208
5,574,928	11/1996	White et al.	712	23

\*<1,2>6,212,630 04/2001 Takayama et al.  
No issue date available.

FOREIGN REFERENCES

<u>Foreign Doc No.</u>	<u>Date</u>	<u>Country</u>	<u>Class</u>	<u>SubClass</u>
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OTHER REFERENCE CITATIONS (incl. Author, Title, Date, Pertinent Pages, etc.)

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U.S. REFERENCES

<u>U.S. Pat No.</u>	<u>Date</u>	<u>Patentee</u>	<u>Class</u>	<u>SubClass</u>
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<1,4>2002/013871509/2002 Minematsu				
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FOREIGN REFERENCES

<u>Foreign Doc No.</u>	<u>Date</u>	<u>Country</u>	<u>Class</u>	<u>SubClass</u>
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OTHER REFERENCE CITATIONS (incl. Author, Title, Date, Pertinent Pages, etc.)

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REFERENCES (Page 8) SERIAL NUMBER: 09/882,285  
FORM 1449

U.S. REFERENCES

<u>U.S. Pat No.</u>	<u>Date</u>	<u>Patentee</u>	<u>Class</u>	<u>SubClass</u>
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FOREIGN REFERENCES

<u>Foreign Doc No.</u>	<u>Date</u>	<u>Country</u>	<u>Class</u>	<u>SubClass</u>
------------------------	-------------	----------------	--------------	-----------------

OTHER REFERENCE CITATIONS (incl. Author, Title, Date, Pertinent Pages, etc.)

[i LSI TinyRisc Development [1 (visited Apr. 24, 2001) [21  
[http://www.redhat.com/support/manuals/gnupro99r1/6\[hd](http://www.redhat.com/support/manuals/gnupro99r1/6[hd) [13 [1  
embed/emb09.html[22 , 13 pages.

-----  
[i VAX11/780 Architecture Handbook[1 , vol. 1, Digital Equipment  
Corporation, 1979, pp. 8-20 through 8-23.

-----  
Hunter, Colin, [37 Series 32000 Programmer[3 s Reference Manual, [38 [0  
Prentice-Hall, Inc., Englewood Cliffs, New Jersey, 1987, pp. 6-81  
through 6-86; pp. 6-164 through 6-167; and pp. 6-183 through 6-184.

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REFERENCES (Page 9) SERIAL NUMBER: 09/882,285  
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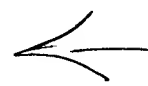
U.S. REFERENCES

<u>U.S. Pat No.</u>	<u>Date</u>	<u>Patentee</u>	<u>Class</u>	<u>SubClass</u>
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------------------------	-------------	----------------	--------------	-----------------

OTHER REFERENCE CITATIONS (incl. Author, Title, Date, Pertinent Pages, etc.)

[37 ARM Architecture Reference Manual, [38 [0 Document No. ARM DDI  
0200B, Prentice Hall, New York, New York, 1996, pp. 6-62 through 6-65. 

-----  
[i CompactRISC[198 [0 CR16B Programmer[3 s Reference Manual[1 ,  
National Semiconductor Corporation, Santa Clara, CA, Sep. 1999, pp.  
Instruction Set 5-44 through Instruction Set 5-47.

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